

2nd INTERNATIONAL CONFERENCE ON ADVANCEMENTS IN POWER, COMMUNICATION AND INTELLIGENT SYSTEMS (APCI 2025)



**Government College of Engineering Kannur
Kannur, Kerala, India**

27th – 28th June 2025

Hybrid Mode *

**VENUE:
Government College of Engineering Kannur**

**Sponsors:
Government College of Engineering Kannur (DTE Kerala)
IEEE Kerala Section (Technical Co-sponsor)**

 <https://www.gcek.ac.in/APCI2025>

 apci@gcek.ac.in

Registrations are open for Non Presenting Participants

Registration fee: Indian Delegate: ₹ 2000+18% GST, Foreign Delegate: \$100+18% GST



Scan for Registration

About APCI

2nd International conference on Advancements in Power, Communication, and Intelligent systems (APCI 2025) will be held in hybrid mode during 27th, 28th June 2025, at Government College of Engineering Kannur, Kerala, India.

This conference is a step to encourage researchers and professionals for sharing the developments in power, communication, signal processing, electronic systems, artificial intelligence and related areas. The participants from academia, industry and research organisations come closer through presentation sessions, keynote lectures and exhibitions. The conference offers chance for researchers to share their novel thoughts related to current and future technologies in power, communication and intelligent systems and hence provide an excellent opportunity for interaction, discussion and dissemination of the latest research and developments.

We invite all leading researchers, engineers and scientists in the domain of interest from around the world. We warmly welcome authors to submit research papers to International Conference APCI 2025, and share their valuable experiences and thoughts with the scientific community.

International Conference on Advancements in Power, Communication, and Intelligent Systems started in the year 2024. First edition of the conference was conducted in hybrid mode on 21st and 22nd June 2024 at Government College of Engineering Kannur, Kerala, India. The conference was organized by Government College of Engineering Kannur with the support of APJ Abdul Kalam Technological University and Department of Technical Education, Kerala. The conference was technically co-sponsored by IEEE Kerala Section. Conference had nine different tracks in which research papers were presented. Tracks were 1. Power and Renewable Energy Systems, 2. Electric Drives and Power Converters, 3. Sensors, Instrumentation and Automation Systems, 4. VLSI, 5. Embedded Systems and IoT, 6. Signal Processing, 7. Communication Systems, 8. Artificial Intelligence and Machine Learning, 9. Cloud Computing.

About GCE Kannur

Government College of Engineering, Kannur is one of the premier institutes among the 9 Government Engineering Colleges in Kerala and was established in 1986. The college is functioning in a sprawling 68-acre scenic campus, having sound and self-sufficient infrastructure, at Mangattuparamba, near the National Highway, 15 km from the headquarters of Kannur district. The college is affiliated to Kerala Technological University. It offers 5 B. Tech degree programs in Civil, Mechanical, Electrical and Electronics, Electronics and Communication and Computer Science and Engineering branches. The College also offers 8 M. Tech programs in Civil Engineering (Computer Aided Structural Engineering, Geotechnical and Geo environmental Energy), Mechanical Engineering (Advanced Manufacturing and Mechanical Systems Design, Energy Engineering), Electrical and Electronics Engineering (Power Electronics and Drives, Power Systems), Electronics and Communication Engineering (Signal Processing and Embedded Systems) and Computer Science and Engineering (Artificial Intelligence and Data Science). Doctoral programs are offered under Kerala Technological University. The institution is recognized by Kerala Technological University as a 'Centre of Excellence in Systems, Energy and Environment'.

Scope of the Conference

Track 1: Power and Renewable Energy Systems:

Smart grid, Power quality, Electronic systems for energy applications, Energy conservation and management.

Track 2: Electric Drives and Power Converters:

Electric Drives, Electric Vehicles, Control and Topologies of Power Converters.

Track 3: Sensors, Control, and Automation Systems:

Power and biomedical applications, Control systems, Instrumentation systems.

Track 4: VLSI:

Analog design, Digital design, CAD based design, Devices and Circuits, Semiconductor devices and sensors, ASIC and FPGA design, Nano-electronics, Low power design, Testing and verification, Quantum Computing.

Track 5: Embedded Systems and IoT:

Processor design, Microprocessor/Microcontroller based design, System design, IoT, Sensors and actuators based systems, Wireless sensor network, Robotics, Embedded protocols.

Track 6: Signal Processing:

Image and video processing, Audio/Speech processing, Biomedical signal processing, Pattern recognition, Signal analysis and modelling, Signal compression and coding, Statistical signal processing, AI/ML in signal processing.

Track 7: Communication Systems:

Wireless/Mobile communication, Optical communication, Satellite communication, Network protocols, Information theory and coding, Cognitive radio, Radar and Antennas.

Track 8: Artificial Intelligence and Machine Learning:

Virtual and Augmented reality, Generative AI and large language model, Natural language processing and machine translation, Social network behaviours, Modelling and analysis, Big data analytics.

Track 9: Cloud Computing:

Cloud computing, High performance computing.

Track 10: Cyber Security and Cryptography:

Cryptography, Cyber security.

* Note: If your work is on AI/ML, select the appropriate track based on the contents and applications of your work.

** Note: Scope of the conference is not limited to the above topics. Authors can select appropriate tracks to submit their papers, relevant to the specific track.

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Programme Schedule

Date	From	To	Program
27 th June 2025	9.30AM	10.45AM	Inauguration
	10.45AM	11:00 AM	High Tea
	11.00AM	11.45AM	Plenary 1 : Revolutionizing IoT with 5G: Innovations and Future Prospects. Speaker: Fabian Sowieja, IICT Germany
	11.45AM	12.30AM	Plenary 2 : Decoding Brain Aging: AI-Powered Insights from MRI. Speaker: Dr. Joseph Suresh Paul, DUK, Kerala
	12.30 AM	1.15 PM	Plenary 3 : Mathematics, Computation, and the Rise of Intelligence. Speaker: Dr. Jafarali P., Scientist, KISR, Kuwait
	1.15PM	2.00PM	Lunch Break
	2.00PM	3.45PM	Presentation Session1: All Presentation Tracks
	3.45PM	4PM	Tea Break
	4PM	6PM	Presentation Session2: All Presentation Tracks
28 th June 2025	9.00AM	11.00AM	Presentation Session3: All Presentation Tracks
	11.00AM	11.15AM	Tea Break
	11.15AM	12.00 PM	Keynote Speech 1: AI in Cybersecurity. Speaker: Dr. Amit Praseed, NIT Calicut
	12.00PM	12.45PM	Keynote Speech 2: Switched capacitor based single stage solar inverter. Speaker: Dr. Nikhil Sasidharan, NIT Calicut
	1.245PM	1.45PM	Lunch Break
	2.00PM	2.30PM	Valedictory Function
	2.30PM	2.45PM	Tea

